Modeling and Automation of Industrial Processes

Modelação e Automação de Processos Industriais / MAPI

Introduction to MAPI laboratory

Prof. Luis Custódio Prof. José Gaspar

Shift 1: Mon 09:30-11:00 Thu 10:00-11:30

Número	Grupo №	Nome
66325	1	Tomás Marques Videira Fonseca
96139	1	Afonso Miguel De Almeida Santos Klier
96329	1	Tiago Filipe Espadinha Simões
93143	2	Miguel Henrique Dos Santos Vicente Alves Nabais
96134	2	Afonso Bispo Certo
96945	3	Sebastião Barroso Dias Gonçalves Chaves
97381	3	João Mendes das Neves Martins
106903	4	Afonso Folgado Soares Amorim de Figueiredo
96297	4	Pedro Afonso Botelho Pires Lopes Dias

Shift 2: Wed 12:30-14:00 Thu 11:30-13:00

Número	Grupo №	Nome
86680	1	Ricardo Beja Belchior dos Santos
105737	1	João Lourenço Mestre Vitorino de Almeida e Paiva
104688	1	Rodrigues de Fátima Jacinto
90058	2	Duarte Mata da Silva Honrado
81595	2	António Martim Carneiro Portugal e Vasconcelos
93150	2	Paulo Luís Santos Cruz
107842	3	Arianna Esposito
105065	3	Carla Juan Cazalla
107886	3	Eirik Berg Wang
93197	4	Tomás Pericão Moreira Roque Pires
93777	4	João Afonso Pacheco de Sousa

Training Laboratory Work

Part A - Introduction to PLC Programming

Part B - Data Logging using the PLC Memory

PLC TSX Premium P57 1634M







toolbar

Editor window



Project browser

Information window

Group folder creation:

Start MatLab 2022

🖙 Run

>> login_mapi <group_number>

If you don't know your group number, just run

>> login_mapi

Find your group number in the list and enter it

This command creates your group folder with the following path (write it down):

C:\users2\mapi\<group_number>

Save all your files and projects in your group folder

Configuration:

In the Lab there are PLCs with two different processors: 1634 or 2634

In the Unity software be sure that the right processor is selected (version 2.00 for both)

 $rac{}{}$ File \rightarrow New

Premium TSX P57 1634M (or 2634)

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	TSX H57 44M	03.10	57-4 Hot-Standby, 2Mb program with PCMCIA, USB, Unitelway		
	TSX P57 0244M	03.10	57-0, 128Kb Program, CANopen		
	TSX P57 104M	03.10	57-1, 224Kb Program, Unitelway		
	TSX P57 154M	03.10	57-1, 224Kb Program, Fipio, Unitelway		
	TSX P57 1634M	03.10	57-1, 224Kb Program, Ethernet-TCP/IP, Unitelway		
	TSX P57 204M	03.10	57-2, 768Kb Program, Unitelway		
	TSX P57 254M	03.10	57-2, 768Kb Program, Fipio, Unitelway		
	TSX P57 2634M	03.10	57-2, 768Kb Program, Ethernet-TCP/IP, Unitelway		
	TSX P57 304M	03.10	57-3, 1.75Mb Program, Unitelway		
	TSX P57 354M	03.10	57-3, 1.75Mb Program, Fipio, Unitelway		
	TSX P57 3634M	03.10	57-3, 1.75Mb Program, Ethernet-TCP/IP, Unitelway		
	TSX P57 454M	03.10	57-4, 2Mb Program with PCMCIA, Fipio, USB, Unitelway		
	TSX P57 4634M	03.10	57-4, 2Mb Program with PCMCIA, Ethemet-TCP/IP, USB, Unitelway		
	TSX P57 554M	03.10	57-5, 7Mb Program with PCMCIA, Fipio, USB, Unitelway		
	TSX P57 5634M	03.10	57-5, 7Mb Program with PCMCIA, Ethemet-TCP/IP, USB, Unitelway		
	TSX P57 6634M	03.10	57-6, 7Mb Program with PCMCIA, Ethemet-TCP/IP, USB, Unitelway		
	TSX P57CA 0244M	03.10	57-0, 128Kb Program, Config 110.,220 VAC		
	TSX P57CD 0244M	03.10	57-0, 128Kb Program, Config 24 VDC		
	TSX PCI57 204M	03.10	57-2 for PC, 768Kb Program, Unitelway		
	TSX PCI57 354M	03.10	57-3 for PC, 1.75Mb Program, Fipio, Unitelway		
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 $rac{1}{2}$ doubleclick module (0)

select tab I/O objects [select Implicits %I and %Q] [update grid with]

Configuration:



doubleclick P



select DEY 16D2 (discrete) or DMY 28FK (module 3)

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Derived FB Types	TSX DEY 16A4	16I 110 120 VAC TR.BLK					
Variables & FB instance	TSX DEY 16A5	16I 220 240 VAC TR.BLK					
Elementary Variables	TSX DEY 16D2	16I 24 VDC SINK TR.BLK					
Derived Variables	TSX DEY 16D3	16I 48 VDC SINK BORN					
Device DDT Variables	TSX DEY 16FK	16I FAST 24 VDC SINK CONN					
IO Derived Variables	TSX DEY 32D2K	32I 24 VDC SINK CONN					
Elementary FB Instance	TSX DEY 32D3K	32I 48 VDC SINK CONN					
Derived FB Instances	TSX DEY 64D2K	64I 24 VDC SINK CONN					
Motion	TSX DMY 28FK	16I 24 VDC, 12Q 24 VDC					
Communication	TSX DMY 28RFK	16I 24 VDC, 12Q REFLEX					
	TSX DSY 08R4D	8Q VDC RELAY TR.BLK					
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	TSX DSY 08122	8Q 24 VDC 2A SRC TR.BLK					
	ISX DSY 08131	8Q 48 VDC 1A SRC TR.BLK					
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Configuration:



S doubleclick (4)

select DSY 16T2 (discrete) or DMY 08R5

1	Unity	Pro	XLS :	<no< th=""><th>name>*</th></no<>	name>*

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(P) : TSX PS	TSX DEX 1645	161 220 240 VAC TR BLK			
🔅 🗓 🧹 0 : TSX P57	TSX DEY 1602	16I 24 VDC SINK TR BLK			
1 : TSX ETY 1	TSX DEV 16D2	16L48 VDC SINK BORN			
2 : TSX DEY	TSX DEY 166K	16LEAST 24 VDC SINK CONN			
3	TSX DET 1011	32I 24 VDC SINK CONN			
4	TSX DEX 32D3K	32L48 VDC SINK CONN			
Derived Data Types	TSX DEX 64D2K	64L24 VDC SINK CONN			
Derived FB Types	TSX DET 0402K	16L24 VDC 12Q 24 VDC			
🔄 Variables & FB instance	TSX DMY 28RFK	161 24 VDC, 120 REELEX			
Elementary Variables	TSX DSY 09P4D	90 VDC RELAY TR BLK			
Derived Variables	TSX DS1 0014D	80 50 VA RELAY TR BLK			
Device DDT Variables	TSX DSY 08R54	80 100 VA RELAY TR BLK			
IO Derived Variables		90 TRIAC 49.240 VAC 24			
Elementary FB Instanc	TSX DS1 0055	80.24 VDC 0.54 SPC TR RLK			
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Configuration:





ETY Port & Channel 0:

Function "ETH TCP IP"

(close the window)

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Project Browser	
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× ×	
∬ I Build ∧ Import/export λ User errors λ FDT log event λ	Search/Replace /
Ready	HMI R/W mode OFFLINE TCPIP:127.0.0.1 NOT BUILT

Run:

rightarrow File \rightarrow Save [be sure to save in your folder c:\users2\mapi\<group number>]

PLC menu select Simulation Mode

 $rightarrow PLC \rightarrow Connect$

rightarrow PLC \rightarrow Transfer project to PLC

(accept "Rebuild")



Unity Pro XLS : <no name="">* le Edit View Services Tools Build</no>	PLC Debug Window Help		
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Variables & FB instances			
Elementary Variables	Update Upload Information		
Device DDT Variables	Update Init Values with Current Values	ues.	
IO Derived Variables	Update Local Init Values with PLC Ir	nit Values.	
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Rebuild All Project A	import/export A User errors A	For log event A	bearch/Re





Animation table:

Click Force

right click variable, option Force, Values: F0 F1 unforce





"New section"

name: my_tst

language: LD

New					×
General	Localization	Condition	Comment		
Name:					
my_tst					
Langua	ge:			Protection:	
ST		\sim		None	~
ST IL FBD SFC LD					
	[ОК	Cancel	Apply	Help

Create a new section

MI R/W m



(add to the animation table %i0.2.3 and %q0.4.3)

 $rightarrow PLC \rightarrow Connect$ (if not connected)

rightarrow PLC \rightarrow Transfer project to PLC

 $rightarrow PLC \rightarrow Run$

Animation table - (right click) %i02.3
Force to 1

new Ladder program:

(Editor window) enter



(test it)



Lab hardware:

- PLC TSX Premium P57 in two different configurations: 1634 and 2634
- PC
- connection between PLC and PC uses a RS485 cable linked to the PC serial port
- Lab Panel with:
 - a 2-position switch (On/Off): On \rightarrow bit 2 = 1
 - a 3-position switch (left/middle/right): left \rightarrow bit 0 = 1, right \rightarrow bit 1 = 1
 - a 12-key 4x3 keyboard (see more next)
 - a buzzer (output bit 0 = 1)
 - a red led (output bit 1 = 1)
 - a yellow led (output bit 2 = 1)
 - a green led (output bit 3 = 1)

• Power supply for the I/O modules



Setup with one input and one output modules



PLC testing:

connect PLC (standard mode)

connect power supply for the I/O modules

change the program to



 \square PLC \rightarrow Connect

rightarrow PLC \rightarrow Transfer project to PLC

 \Im PLC \rightarrow Run



(guess what this program does)

PLC testing:

change the program to



rightarrow PLC \rightarrow Transfer project to PLC

 $rightarrow PLC \rightarrow Run$

(guess what this program does)

2

5

8

4

7

Lab keyboard:



In order to identify that a key has been pressed, first we have to energize each one of the keyboard columns through the output bits 4 to 6, and then read the input bits 4 to 7

For instance, if %q0.4.4 is set to 1 (and both %q0.4.5 and %q0.4.6 are zero) and %i0.2.6 is 1, then key "7" is pressed

Data Logger

Procedure to test the program:

- download the zip file <u>data_log_up13.zip</u> by running >> <u>get_files logger</u> using Matlab (also available at http://users.isr.ist.utl.pt/~jag/course_utils/plc_log/data_log_up13.zip)
- Decompress the downloaded zip file to your group folder (extract, not double click)
- the zip file contains already one log file "data_log2_tmp.dtx"
- Using Matlab change dir to your group folder and run: >> mem_dump_load_tst and observe the produced plots





Procedure to test the program:

- Using Unity Pro, open the file **data_log2.stu**, transfer the code to the PLC simulator, and run it.
- Double-click on section 'tst_data_log' and try to interpret the code

- Set variable %m92 to 1 to start the log process
- Check value of variable %mw96
- Wait until %mw96 = 20
- using Unity Pro, export the memory to a file, PLC -> "Save data from PLC to file".
 Save data into the file "data_log2_tmp.dtx" (overwrite it) in your group folder.

```
if %m93 then
  (* log changes on inputs %i0.2.0 .. %i0.2.15 *)
 mem log inputs();
end if;
if %m92 then
  (* log changes on memories %m100 .. %m115 *)
 mem log mem();
end if;
(* test buffer full and place this info in %m91 *)
if %mw96 >= 2*MOD(%MW99,1000) then
 &m91 := 1;
else
 %m91 := 0;
end if;
(* delete logged data *)
if %m90 then
  %mw97 := 0;
  (* reset will be done in the next call to mem log mem() *)
 &m90 := 0;
end if:
```

Procedure to test the program:

- Within Matlab run again: >> mem_dump_load_tst and observe the new plots
- Reset the log memory by setting the adequate variable
- Start a new log process and observe the changes in the produced plots

Procedure to test the program:

- Double-click on section 'square_wave' and try to interpret the code

- Set variable %m97 to 1
- Reset the log memory
- Start a new log process and observe the changes in the produced plots

```
(* Use one timer for all square waves *)
   (IN := NOT(%M99) (*BOOL*),
      PT := t#0.5s (*TIME*),
       Q => %M99 (*BOOL*) );
(* Update the output whenever TON 0 shows a timeout *)
IF %M99 THEN
  (* output a 1Hz square wave to %q0.4.1 *)
 %q0.4.1 :=NOT(%q0.4.1);
  (* %m97=0 make square waves, %m97=1 pulse even bits *)
  (* if you want, output all zeros by setting %m98 *)
 if %m98 then
   %mw90 := 0;
 end if;
 WORD TO BIT( int to word(%mw90),
   %m100, %m101, %m102, %m103,
   %m104, %m105, %m106, %m107,
   %m108, %m109, %m110, %m111,
   %m112, %m113, %m114, %m115);
 if NOT(%m97) then
    (* udate counter to make square waves in memory %m100..%m115 *
    %mw90 := %mw90 +1;
 else
    (* pulse even bits in memory %m100..%m115 *)
   if %mw90=0 then %mw90:=1;
   else %mw90:= ROL(%mw90,2);
   end if;
 end if;
END IF;
```

- Solve item B3 using the PLC

Use the two switches in Lab panel to make a sequence of states 00, 01, 11, 10, 00, 01, 11, 10, 00, each one taking about a couple of seconds. Make a data log (plot) showing each state of the sequence.



